

# Technical Specifications of Standalone Switch QSFP-DDODM

QSFP112 specification has been forwarded to the SNIA TA TWG and chapter 5 and 9 are not being updated and will be removed when SNIA TA specification has been published.

July 11, 2019 - QSFP-DD Hardware Specification for QSFP DOUBLE DENSITY 8X PLUGGABLE TRANSCEIVER - Rev 5.0 May 8, 2019 - Common Management Interface Specification - Rev 4.0

The 9300 32D and 9300S Switch Series will shutdown a port that may be exceeding the maximum temperature for a transceiver - this is to prevent damage to the transceiver and other components on ...

The FS QSFP-DD Digital Coherent Optics (DCO) transceiver supports 400G coherent transmission for data center interconnect and metro/edge applications.

1 Scope The scope of this specification is the definition of high-speed/density 4 and 8 electrical lanes (4x, 8x) modules, cage and connector system. The QSFP-DD and QSFP112 both supports up to 400 ...

embles Temp-Flex cable technology Boosts electrical performance. Provides excellent operational margin, short . end-user cost via manufacturing efficiencies 32 and 30 AWG cab. es Fulfills all ...

QDD-4ZQ100-CuxM cables are suitable for very short links and offer a cost-effective way to establish a 4x 100-Gigabit link between QSFP-DD 400G port and four 100GE ports to switch or servers within or ...

Powered by Greylock and Delphi DSP ASICs, and silicon photonic integrated circuits (PICs) for an optimized co-packaged design with 3D Siliconization. Supports an expansive list of interoperability ...

TE Connectivity's (TE) QSFP-DD (quad small form-factor pluggable double density) doubles the density of QSFP interconnects with an eight-lane electrical interface capable of 28 Gbps NRZ or 56 Gbps ...

Amphenol's QSFP-DD high-speed connector family features a scalable, high-performance interconnect platform with 76 contacts on a 0.8mm pitch and a dual-mating interface. ...

The QSFP-DD interconnect system is comprised of a 76-position, 0.8mm pitch connector built for use in high speed serial applications. Each port supports up to 400 Gbps in aggregate over an 8 x 50 Gbps ...

This specification defines: the electrical and optical connectors, electrical signals and power supplies, mechanical and thermal requirements of the pluggable QSFP Double Density (QSFP-DD) module, ...

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