

The design presented here uses the board's SFP0 interface to connect with another board with the help of an optical transceiver module and an optical fiber, at a data rate of 5 Gb/s (connected as shown in ...

The GTH and GTY transceivers provide the low jitter required for demanding optical interconnects and feature world class auto-adaptive equalization with PCS features required for difficult backplane ...

GTY transceivers support data rates up to 26.5625 Gb/s. The minimum data rate for all transceivers is 1.2 Gb/s, but lower data rates can be achieved by using oversampling in the programmable logic.

Describes the GTY transceivers in the AMD UltraScale(TM) architecture-based devices.

Dual-fiber optical modules are currently available in various transmission rates, such as 1.25Gbps, 10Gbps, 25Gbps, 40Gbps, 56Gbps, 100Gbps, and 400Gbps. Depending on the type of ...

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AMD's IBERT tool enables an automated self-measurement of a GTH channel's eye diagram when used in a loopback mode. Eye diagrams for two different speeds were captured using this tool with a ...

This User Guide provides comprehensive information about the Xilinx UltraScale GTY transceivers, covering their features, configuration, and use models. Learn about line rates, supported protocols, ...

Via the serial data ports RXP TXP, it can directly connect QSFP optical or CR-type transceivers. Ethernet data are passed to/from FPGA fabric via the standard MII interface.

The GTY transceiver is highly configurable and tightly integrated with the programmable logic resources of the Versal architecture. The following table summarizes the features by functional group that ...

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